Thank you for purchasing a TAPR product. The associated software and directions can be obtained from the web. Here is some basic information and a link to the documentation:

## The T**ADD-2 Pulse-Per-Second Divider**

The **TADD-2** is a divider that accepts a 5 or 10 MHz input signal and generates output frequencies in decade steps from 1 pulse-per-second ("PPS") to 10K PPS.

The pulse train can be synchronized to an external source (such as a GPS receiver).

The TADD-2 has six low-impedance outputs that deliver greater than 3.5 volts into a 50 ohm load, with rise time at the connector of less than 3ns.

The pulse rate of each output can be individually set, along with the output polarity. A wide-range input circuit accepts signal levels as low as -10 dBm. The input can be terminated in 50 ohms or a high impedance load.

The 12 volt power input is fused and protected against reverse polarity. Current drain depends on the number of loads and ranges from 70 to 250ma.

The TADD-2 uses a PIC chip as the divider. The PIC runs code written by Tom Van Baak and modified by Richard McCorkle. The TADD-2 comes with a programmed PIC, and the source code is available as Open Source Software for those who would like to experiment with it.

Manual: <https://web.tapr.org/~n8ur/TADD-2_Manual.pdf>

Contact us at **contact@tapr.org** for assistance, help or troubleshooting.

Best Regards, TAPR